IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	09/932,216
Applicant	:	Yang
Filed	:	August 17, 2001
TC/A.U.	:	2816
Examiner	:	Hiltunen, Thomas
Title:		BOOTSTRAP MODULE FOR MULTI-STAGE CIRCUIT
Docket No.	:	2550/111
Customer No.	:	5542

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service in an envelope as first class mail with sufficient postage addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on June 29, 2006.

Alexander J. Smolenski, Jr.

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Response G

Dear Sir:

This response is being filed along with a Request for Continued Examination and is responsive to the final office action of March 7, 2006 and the Advisory Action dated June 7, 2006. Applicant makes note that the proposed amendment dated May 6, 2006 was not entered. Applicant responds as follows:

Claims begin on page 2 of this paper, and

Remarks begin on page 7 of this paper.

An **Appendix** is attached following page 8 of this paper, including: page 55 of Weste, Neil and Eshraghian, Kamran, *Principles of CMOS VLSI Design*, Addison-Wesley, Reading, Massachusetts, 1985; and page 1-6 of Rung-Bin Lin, Chapter 1: Introduction to CMOS Circuits, http://vlsi.cse.yzu.edu.tw/education/vlsi_design/chapter1.pdf).